

FIG. 1

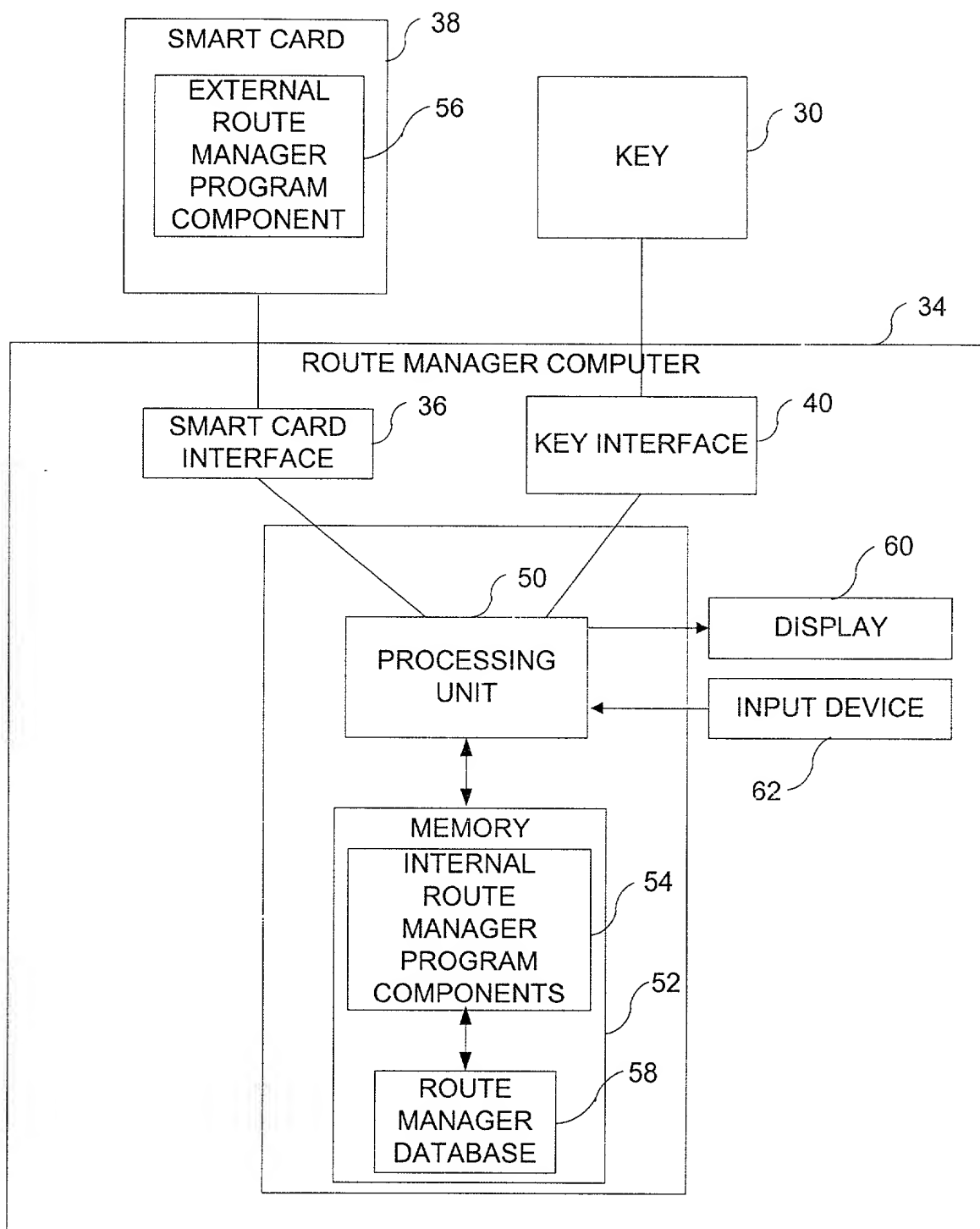


FIG. 2

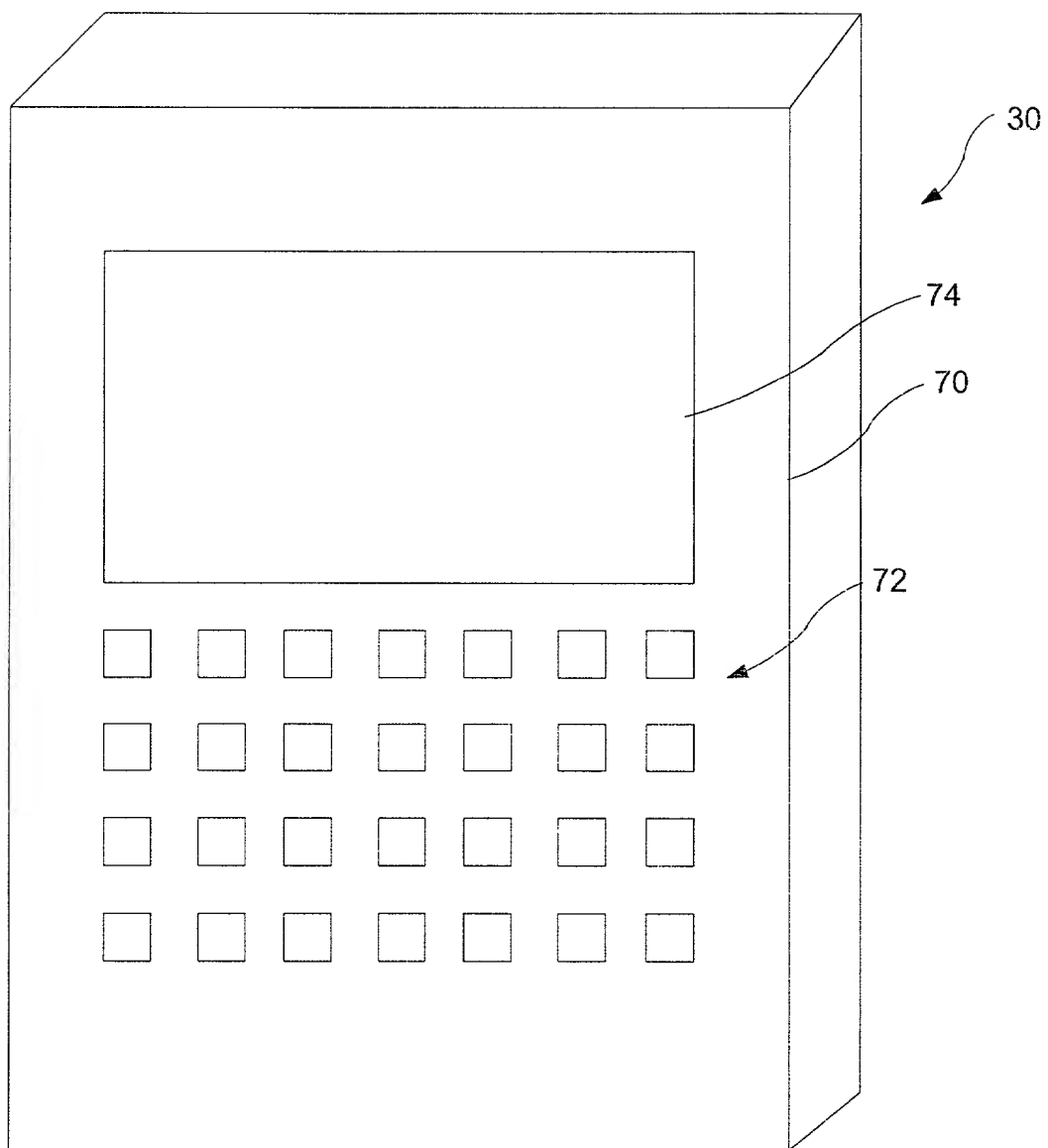


FIG. 3

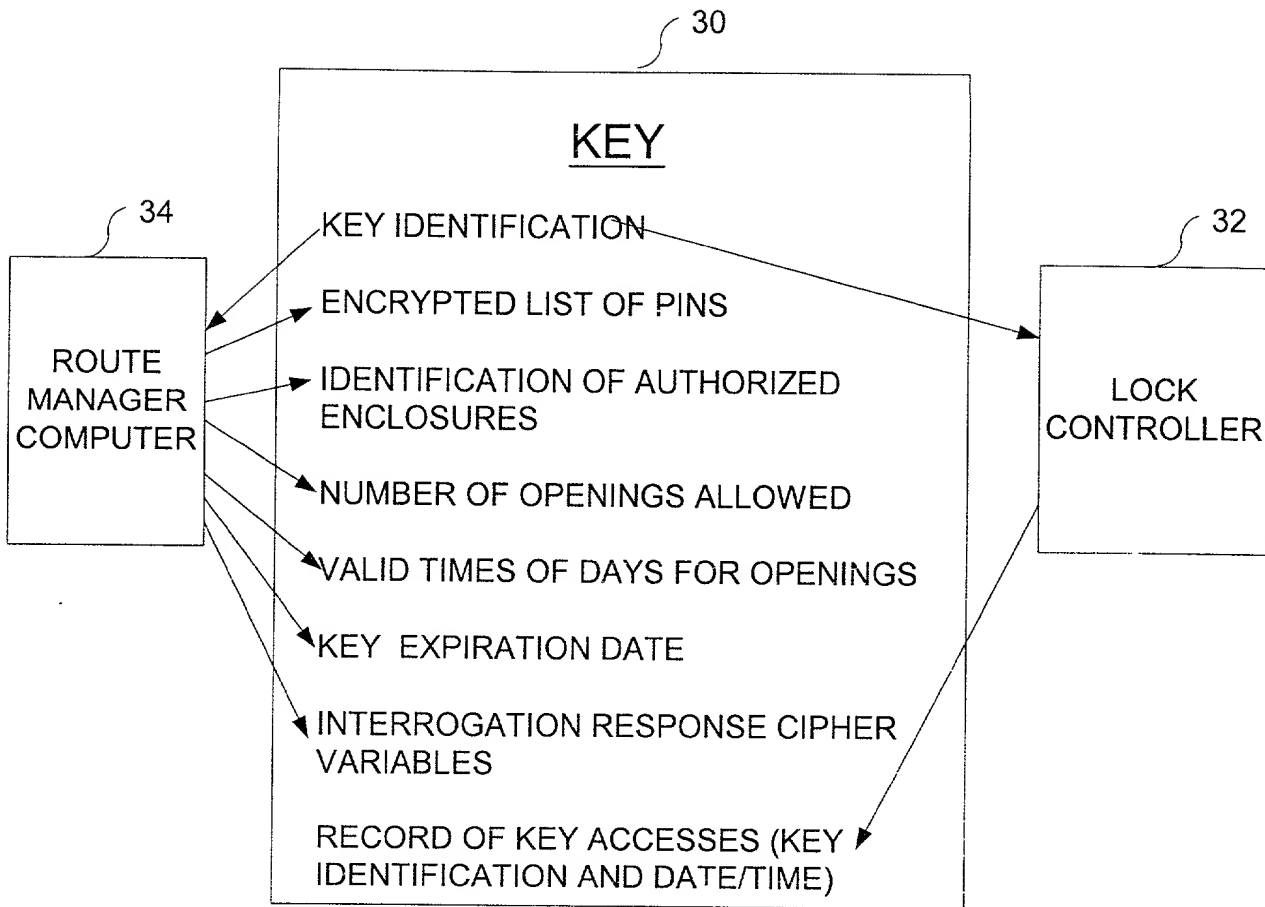


FIG. 4

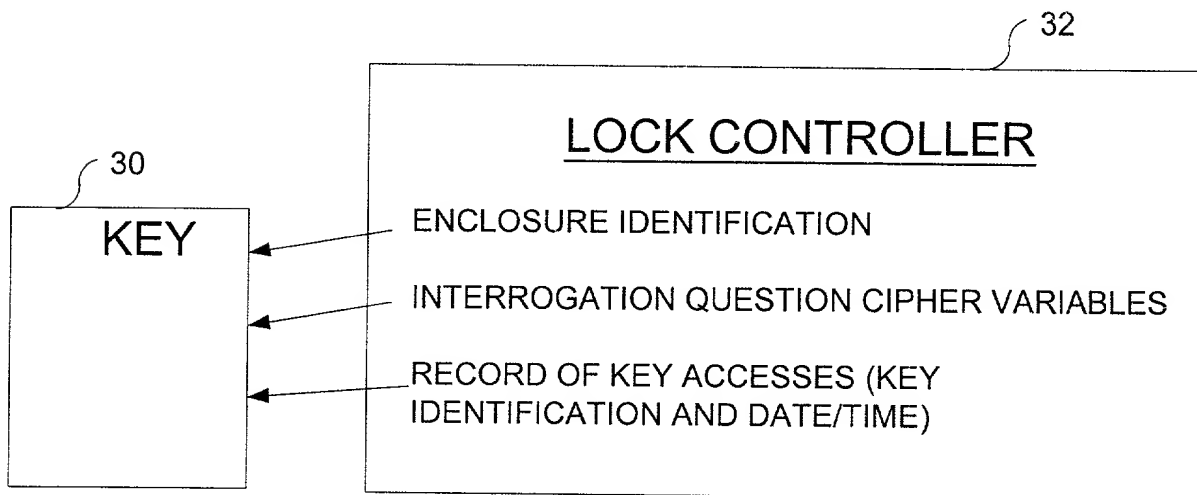


FIG. 5

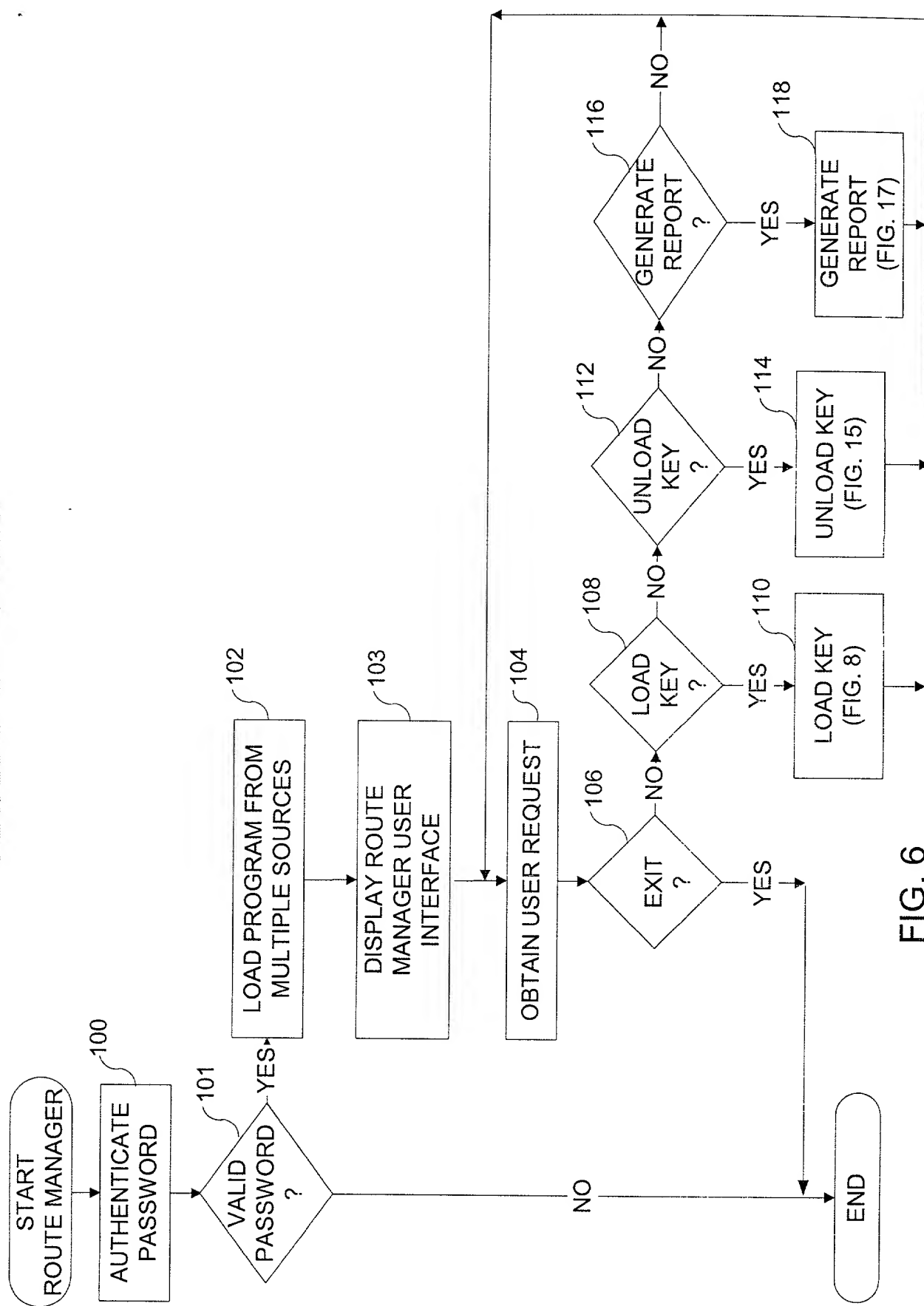


FIG. 6

ROUTE MANAGER PROGRAM

FILE DATABASE CONFIGURATION REPORTS HELP

LOAD

UNLOAD

REPORTS

HELP

EXIT

01/01/2000

FIG. 7

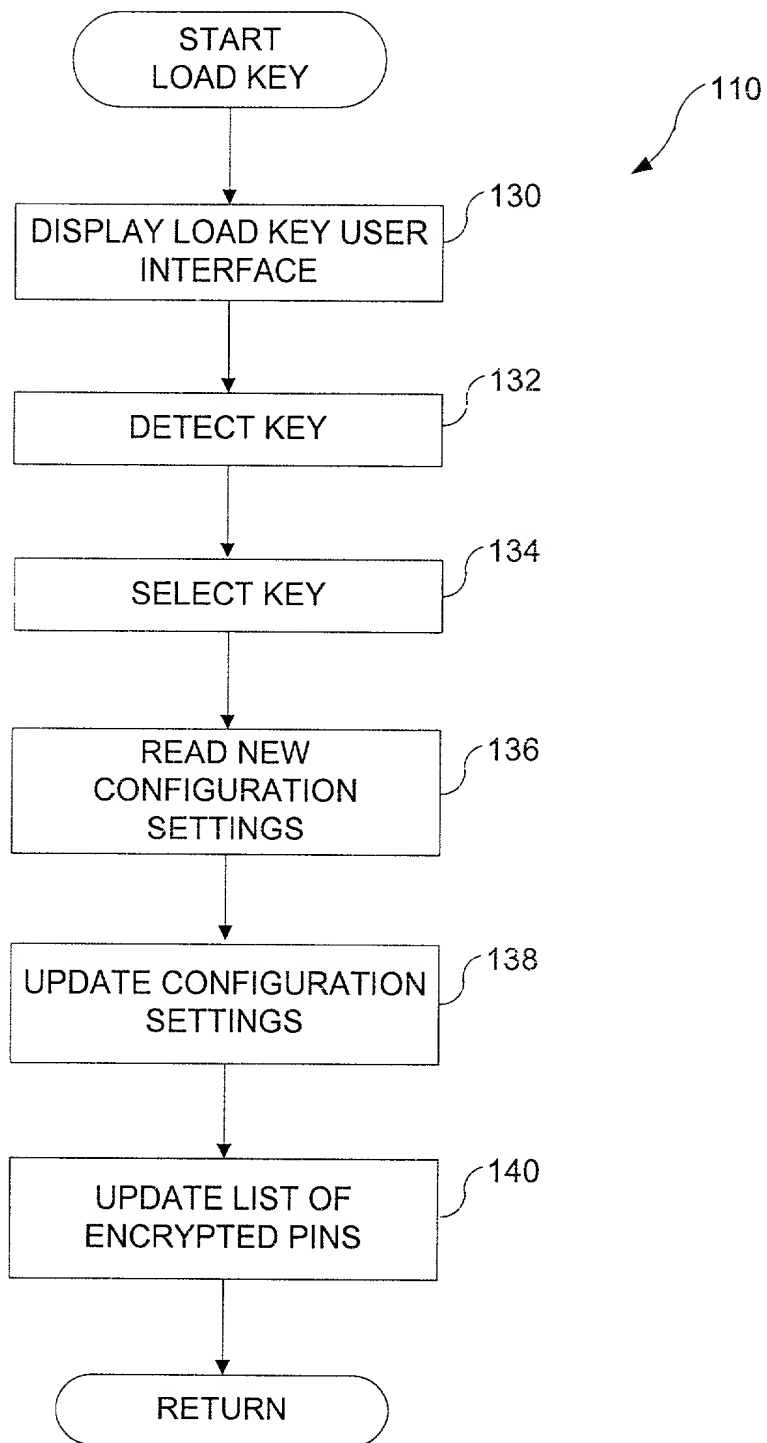


FIG. 8

04260" 0522650

LOAD KEYS			
KEY ID NUMBERS	ROUTE NUMBERS	EMPLOYEE NAME AND NUMBER	KEY ACCESS TIME START TIME STOP TIME
K0001	R0001	JOHN SMITH - E0001	6AM <input type="text"/> 6PM <input type="text"/>
K0002	R0002	JANE DOE - E0002	
K0003	R0003	JIM JONES - E0003	
K0004	R0004	JOHN DOE - E0004	
K0005	R0005		
K0006	R0006		
K0007	R0007		
K0008	R0008		
K0009	R0009		
K0010	R0010		

NUMBER OF USES

KEY EXPIRATION

HELP

GO

CANCEL

FIG. 9

The diagram illustrates a portable digital frequency counter circuit. Key components include:

- Power Supply:** An inductive power source (L1, L2) feeds into a voltage divider (R1, R2, R3) and decoupling capacitors (C1, C2, C3, C4, C5, C6).
- Microcontroller:** U5 (DS5000T) is the central processing unit, interfaced with a keypad (KEYPAD) via I/O pins.
- Logic and Timing:** Various logic gates (U1, U2, U3, U4, U7) and comparators (U8, U9) are used for signal processing. Timing is controlled by resistors (R1-R5) and capacitors (C1-C6).
- Output Stage:** A speaker (LS) provides audio feedback, while a display driver (U6, DS275) drives the output display.
- Connectivity:** A RETARDER CONNECTOR is provided for external connections.

FIG. 10

2

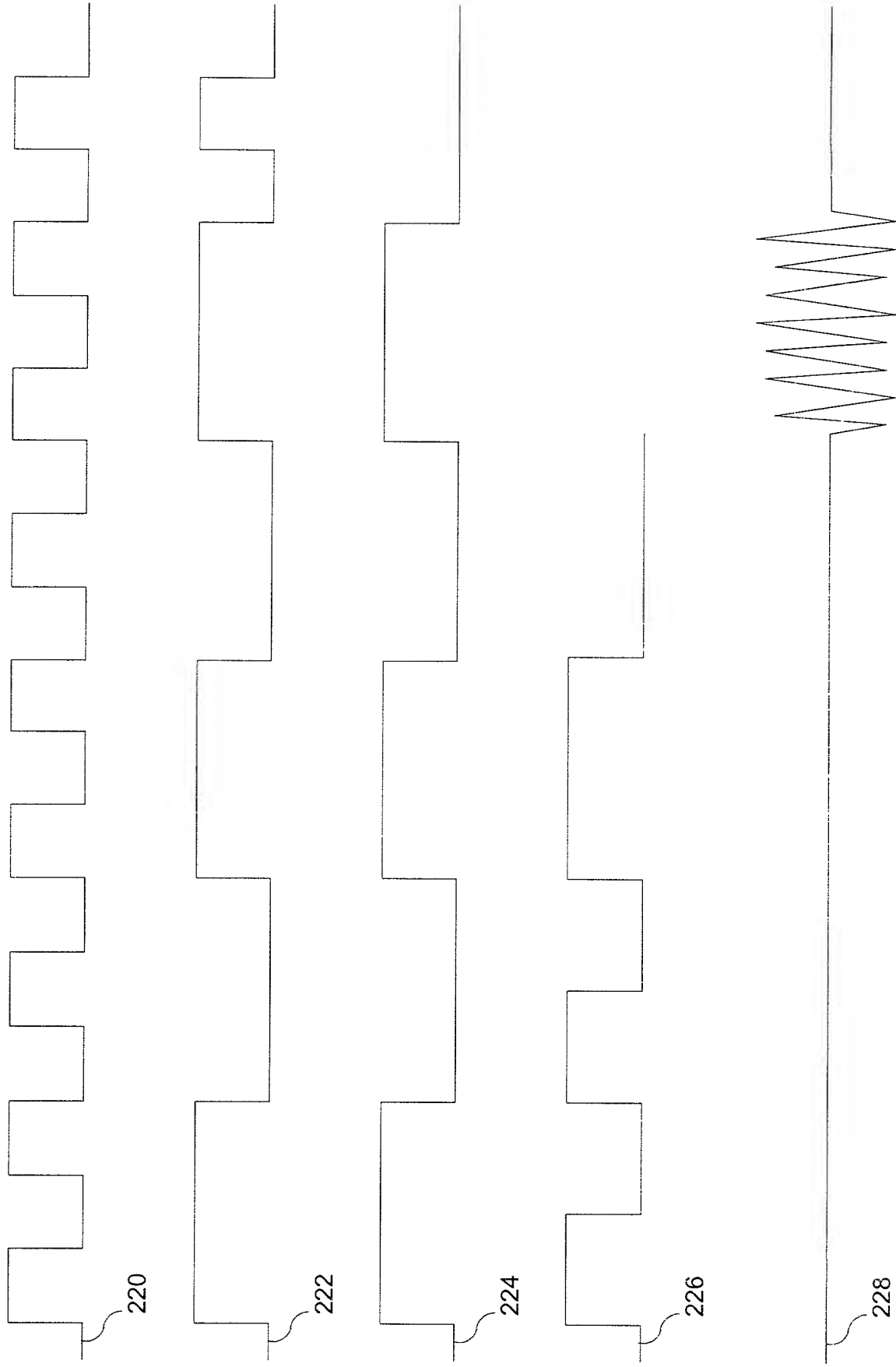


FIG. 12

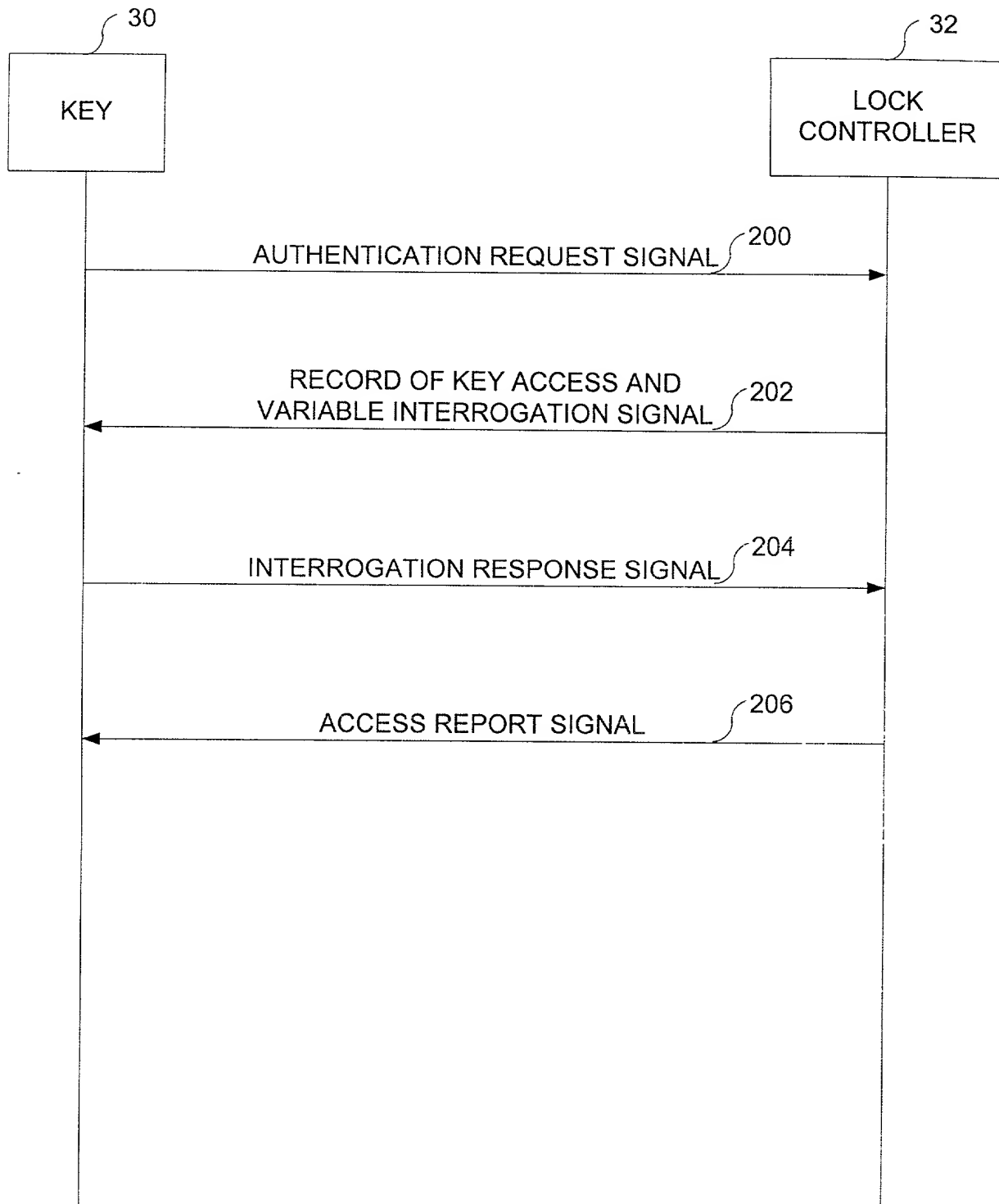


FIG. 13

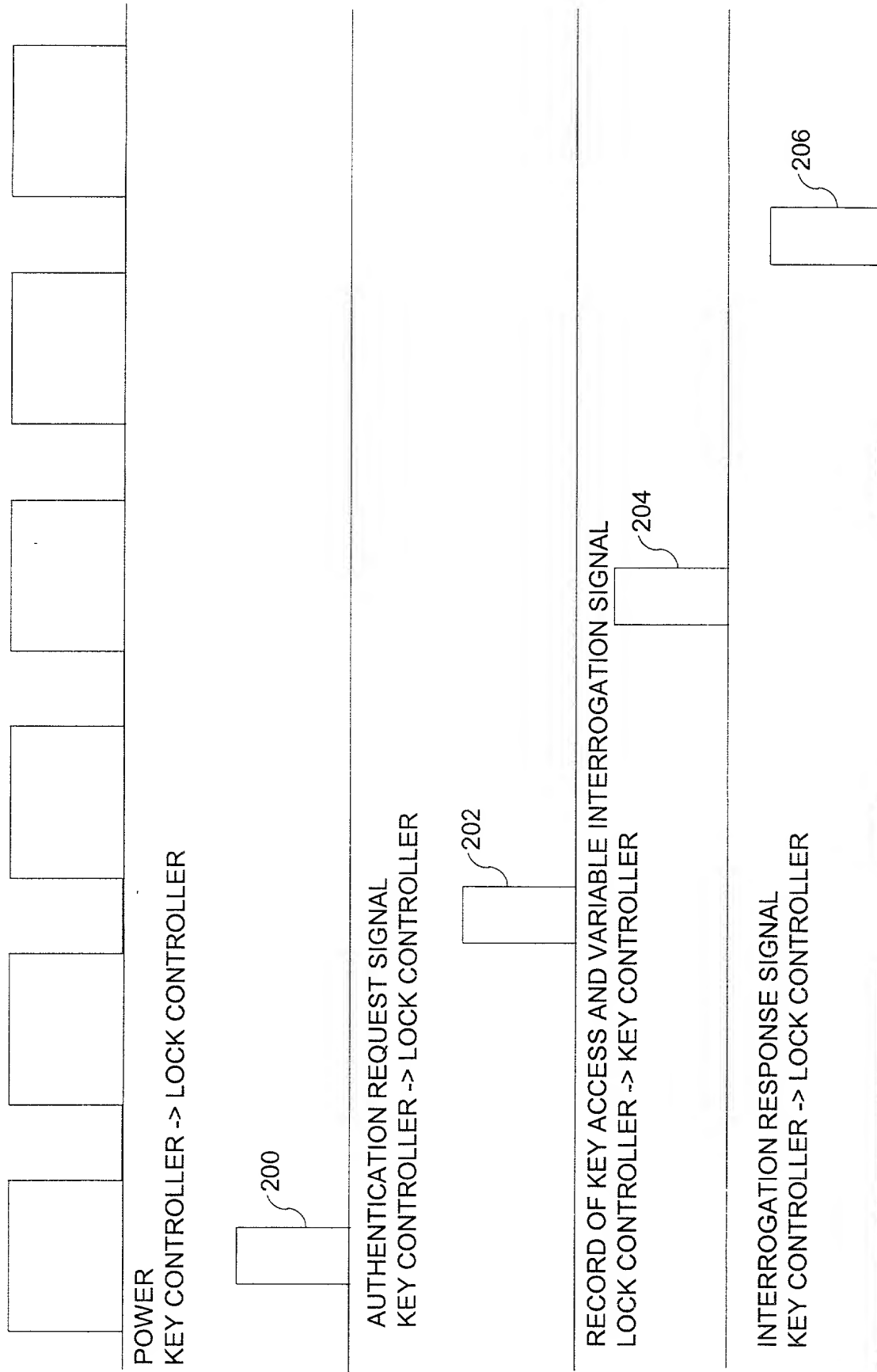


FIG. 14

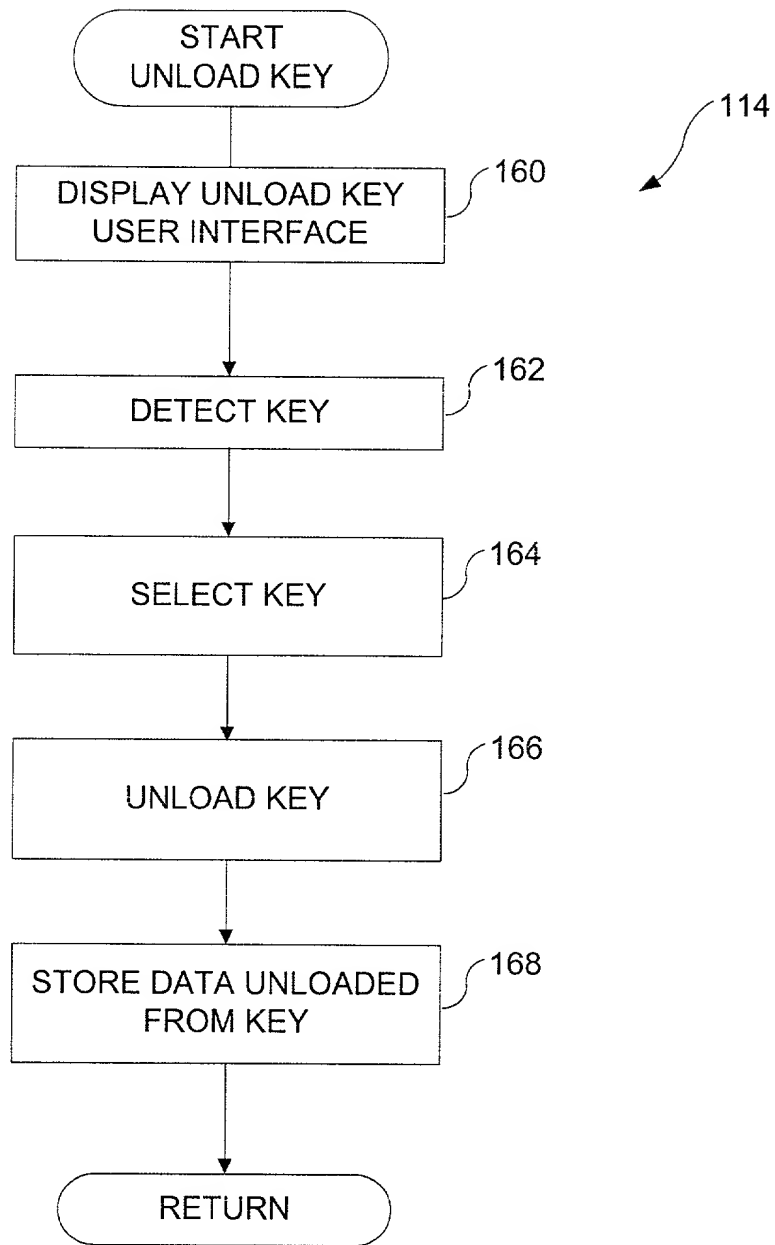


FIG. 15

UNLOAD KEYS

KEY ID
NUMBERS

K0001
K0002
K0003
K0014
K0015
K0016
K0017
K0018
K0019
K0020

UNLOADING STATUS

UNLOADING KEY K0014
150 RECORDS UNLOADED FROM KEY K0014
KEY K0014 HISTORY CLEARED

HELP

GO

CANCEL

FIG. 16

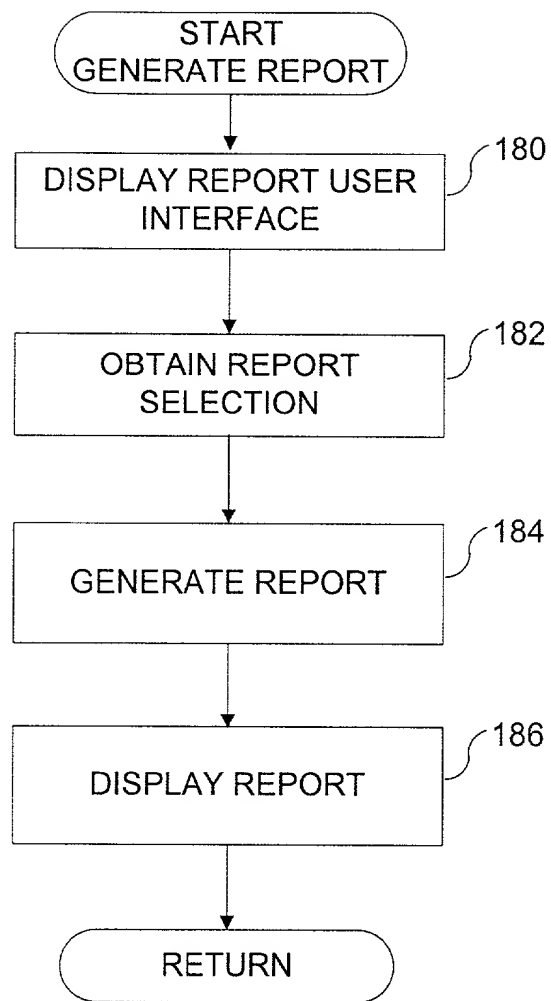


FIG. 17

REPORTS

REPORT TYPE

☐ DETAIL ☐ BY KEY ☐ BY MACHINE ☐ BY EMPLOYEE

START DATE NOVEMBER 1, 1999

STOP DATE NOVEMBER 30, 1999

HELP

GO

CANCEL

FIG. 18

REPORTS

DATE	MACHINE ASSET NUMBER	EMP. NUMBER	KEY ID	TIME	▲
11/07/99	SC32014424	E0004	K0001	08:22:00 AM	
11/08/99	SC32014424	E0004	K0001	08:22:00 AM	
11/09/99	SC32014424	E0004	K0001	06:33:00 AM	
11/10/99	SC32014424	E0004	K0001	03:55:00 AM	
11/11/99	SC32014424	E0004	K0001	06:55:00 AM	
11/07/99	SC32014425	E0003	K0002	10:33:00 AM	
11/08/99	SC32014425	E0003	K0002	10:33:00 AM	
11/09/99	SC32014425	E0003	K0002	05:22:00 AM	
11/10/99	SC32014425	E0003	K0002	12:45:00 PM	
11/11/99	SC32014425	E0003	K0002	05:22:00 AM	
11/07/99	SC32014426	E0002	K0003	03:20:00 AM	
11/08/99	SC32014426	E0002	K0003	09:55:00 AM	
11/09/99	SC32014426	E0002	K0003	09:55:00 AM	
11/10/99	SC32014426	E0002	K0003	04:55:00 AM	
11/11/99	SC32014426	E0002	K0003	04:33:00 AM	
11/07/99	SC32014427	E0001	K0004	06:30:00 AM	
11/08/99	SC32014427	E0001	K0004	01:55:00 AM	
11/09/99	SC32014427	E0001	K0004	06:47:00 AM	
11/10/99	SC32014427	E0001	K0004	06:22:00 AM	▼

CLOSE

PRINT

FIG. 19